

FIG. 1
(Prior Art)

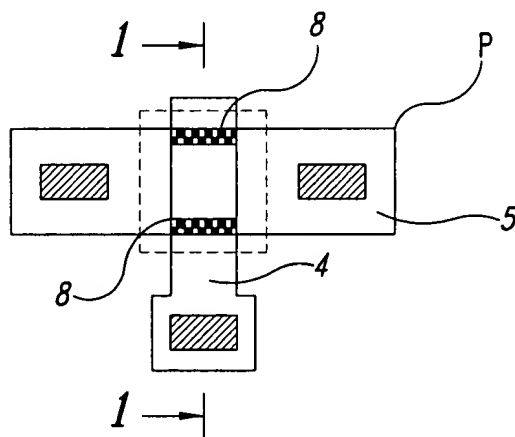


FIG. 2
(Prior Art)

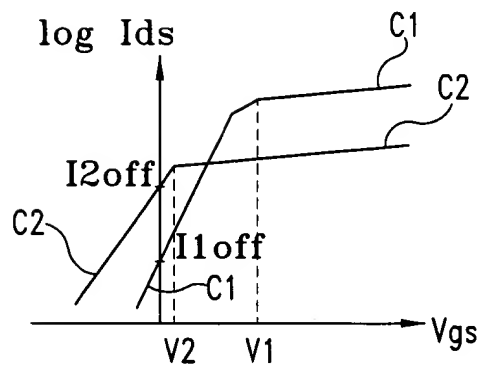


FIG. 3
(Prior Art)



Title: MOS TRANSISTORS IN AN INTEGRATED CIRCUIT AND ACTIVE AREA FORMING METHOD
Express Mail No. EV33652449US "REPLACEMENT SHEET"
Inventor(s): Walter De Costor et al. Serial No. 09/823,274 Docket No. 859063.491

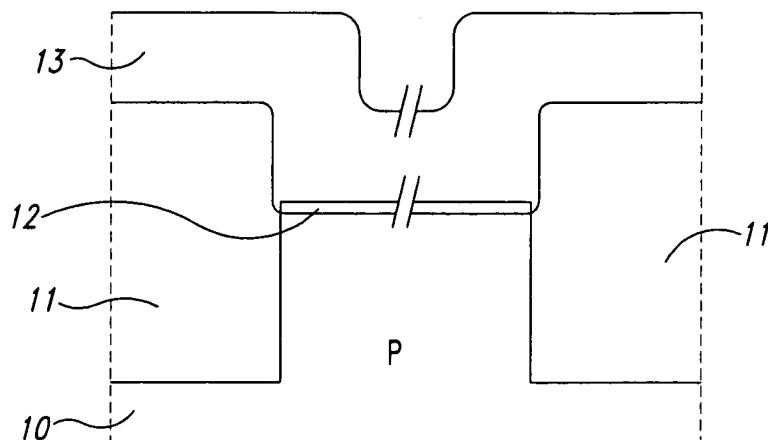


FIG. 4A

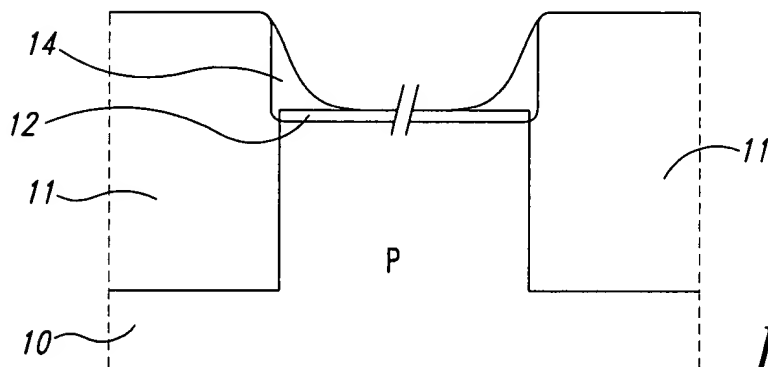


FIG. 4B

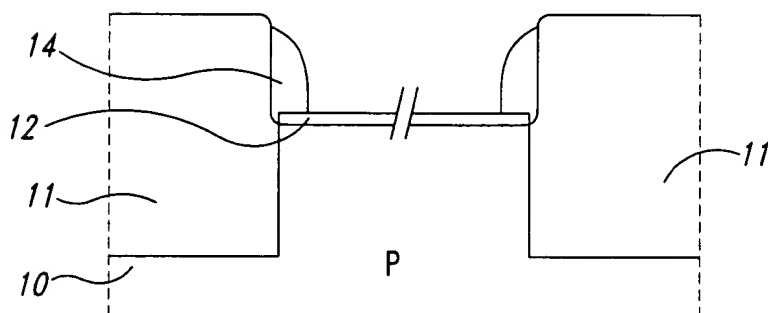
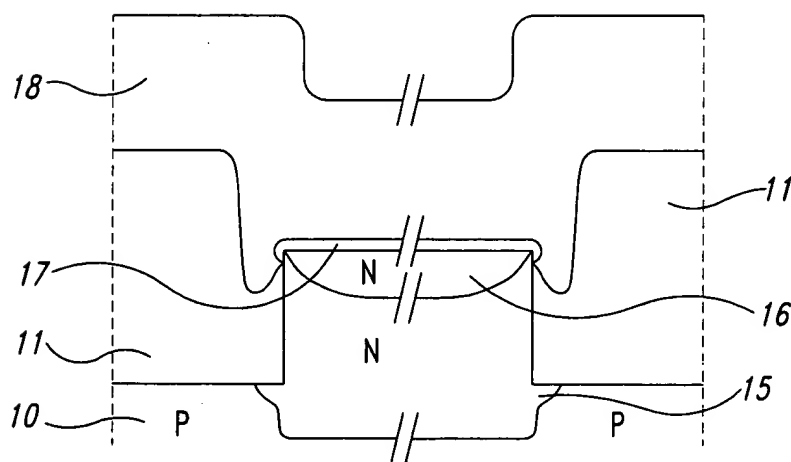
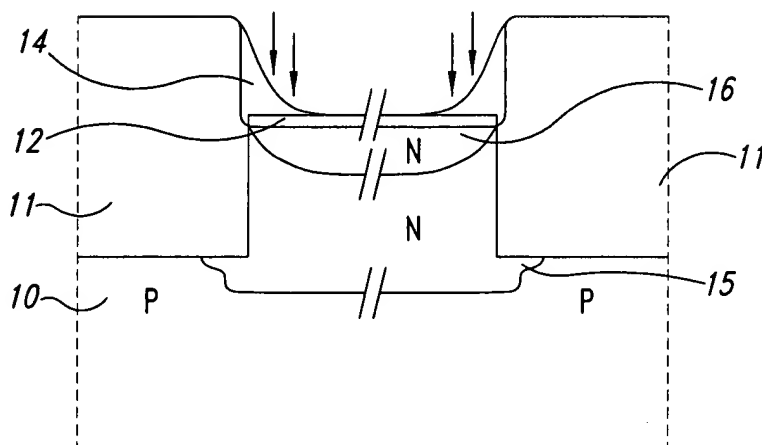
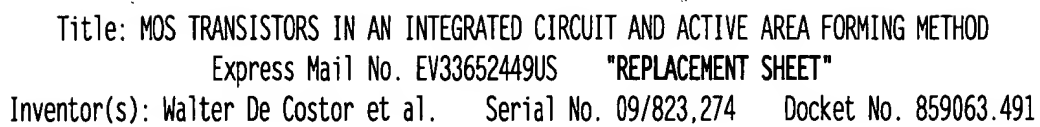


FIG. 4B'



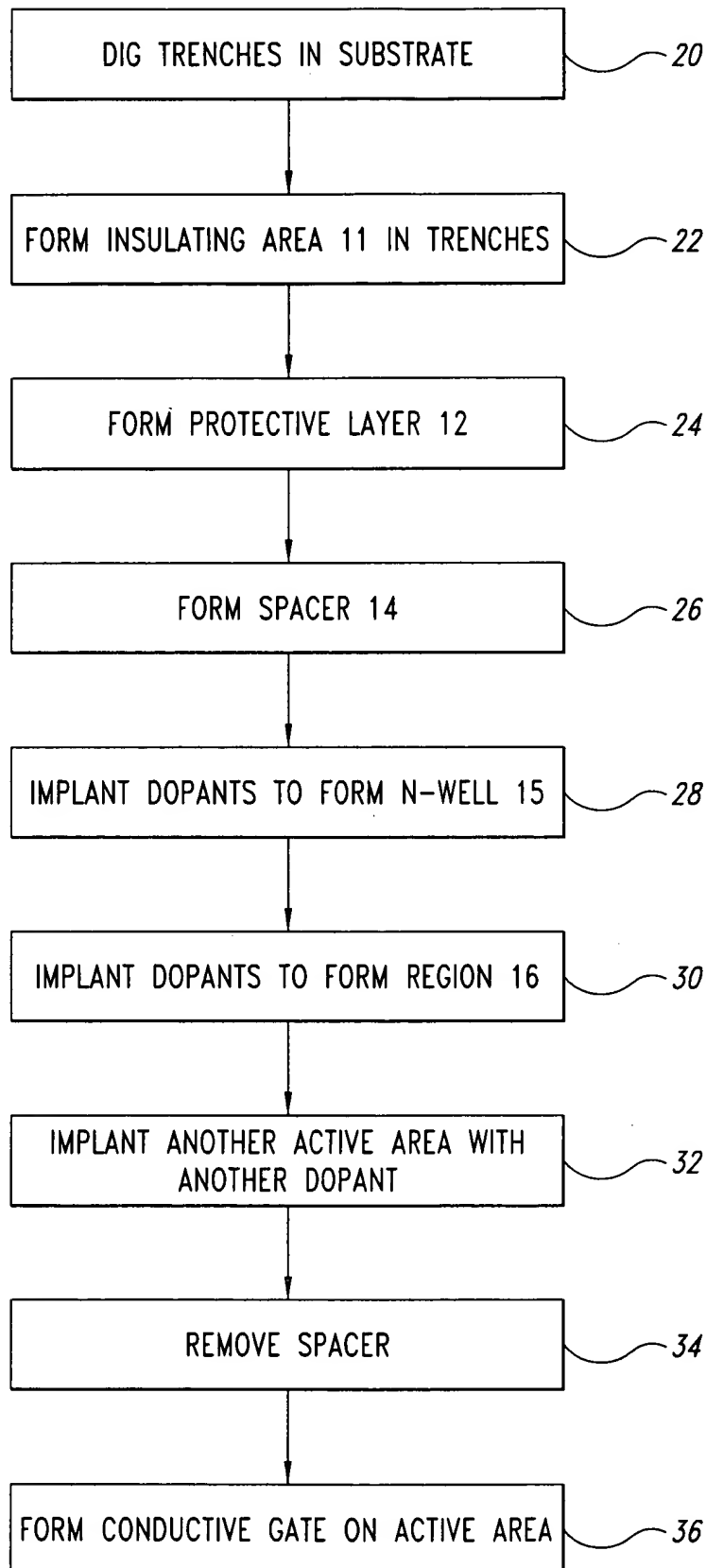


FIG. 5